[5560] - 559

T. E. (E & TC)
ADVANCED PROCESSORS
(2015 Pattern) (Semester - II)

Time : 2½ Hours]   *[Max. Marks :70*

Instructions to the candidates:
1) *All questions are compulsory.*
2) *Neat diagrams must be drawn wherever necessary.*
3) *Assume suitable data, if necessary.*

Q1) a) Explain programmers model of ARM7 with the help of neat diagram.
[6]

b) List various GP10 register & Explain function of each GP10 register.**[6]**

c) Interface 16×2 LCD with LPC 2148. Write. Embedded C program to display sppu on 1st line and PONE of second line. **[8]**

OR

Q2) a) Write an embedded 'C' program to receive data on UARTO of ARM processor. **[6]**

b) List and explain various Addressing modes of ARM7 with the help of proper example. **[6]**

c) Draw and Explain Tiva TM4C1234. **[8]**

Q3) a) Write Embedded 'C' program for GSM module Also display message "University of Pune". **[8]**

b) Interface SD card with LPC 2148 also explain the How diagram in detail. **[8]**

OR

Q4) a) Explain the Interfacing of GPS with LPC 2148. **[8]**

b) Draw and Explain I2C based EPROM Interfacing of LPC 2148. **[8]**

P.T.O.

Q5) a) Explain Extended parallelism and SIMD in details. **[8]**

b) Write note on FIR. FFT of DSP processor. **[10]**

OR

Q6) a) Explain Data pater & register file in detail. **[10]**

b) Write note on VLIW and Von Neuman. **[8]**

Q7) a) Explain with examples the addressing modes of C67X processor. **[8]**

b) Describe interrupt structure of C67X processor. **[8]**

OR

Q8) a) Write note on Fetch packet and EMIF **[8]**

b) Write note on chip peripheral & Internal memory. **[8]**

❋ ❋ ❋