Total No. of Questions : 6]

SEAT No. :

P137

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OCT./BE/Insem-62
B.E. (Electronics)
Advanced Measurement Systems
(2012 Pattern) (Semester - I)

Time : 1 Hour] *[Max. Marks : 30*

Instructions to the candidates:

Solve Q.1 or Q.2, Q.3 or Q.4 and Q.5 or Q.6.

Q1) a) Explain testing procedure using arbitrary signal generator. **[5]**

b) State signal integrity design issues and explain any two in detail. **[5]**

OR

Q2) a) State and explain electrical validation of MSO series oscillosope. **[5]**

b) Differenciate between mixed signal generators and Logic signal generators. **[5]**

Q3) a) With suitable block diagram explain working of Logic analyzer. **[5]**

b) Explain super heterodyne method of spectrum analyzer. **[5]**

OR

Q4) a) Draw and explain network analyzer. **[4]**

b) State applications and Limitations of different types of analysis. **[6]**

Q5) a) Draw and explain interfacing of LCD typical embedded processor. **[6]**

b) Explain embedded communication using CAN. **[4]**

OR

Q6) a) State and explain various AT commands used in GSM communication. **[5]**

b) Explain role of electronic measurments for debugging of automotive Electronics. **[5]**

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