



	InSem Examination-IISummer2024		
	Exam Seat No.:		
	Academic Year:2023-2024	Semester:IV	
	Name of Programme: S. Y. B.Tech (Computer Engineering)	Pattern:2022	
	Name of Course:Computer Architecture	Course Code:COM222014	
	Max. Marks:30	Duration:1 Hr	
	Instructions: Candidates should read carefully the instructions printed on the Question Paper and on the cover page of the Answer Book, which is provided for their use. 1. This question paper contains TWO pages. 2. Answer to each new question is to be started on a new page. 3. Assume suitable data wherever required, but justify it. 4. Draw the neat labelled diagrams, wherever necessary. 5. The last columns indicates the Course Outcome and level of Blooms Taxonomy of the Question/sub-question.		

Question No. 1 Attempt following Question

- a) Represent 12.5 in single precision and double precision format. (5) CO1

OR

- b) Represent -45 using fixed point representations. (5) CO1

- c) Write a short note on Register transfer. (5) CO1

OR

- d) Draw Bus system for four register of four bits. (5) CO1

- e) Draw a diagram to represent Binary incrementer. (5) CO1

OR

- f) List and Explain any five logic microoperation. (5) CO1

Question No. 2 Attempt following Question

- a) Explain stored program organization. (5) CO2

OR

- b) Explain following registers: IR, AC. (5) CO2

- c) Demonstrate direct and indirect address with diagram. (5) CO2

OR

d) Explain following instructions: LDA, ADD. (5) CO2

e) Draw and explain micro programmed control organization. (5) CO2

OR

f) Draw flowchart for interrupt cycle. (5) CO2

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